

OTHER DOCUMENTS

(Including Author, Title, Date, Pertinent Pages, Etc.)

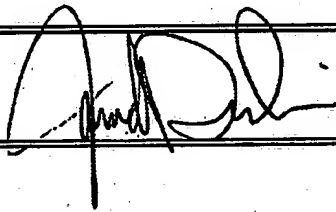
"Dynamic Safe-Area Protection for Power Transistors Employs Peak-Temperature Limiting" by Robert J. Widlar and Mineo Yamatake, IEEE Journal of Solid-State Circuits, Vol. SC-22, No. 1, February 1987, pp. 77-84

"A 15-W Monolithic Power Operational Amplifier" by Paul R. Gray, IEEE Journal of Solid-State Circuits, Vol. SC-7, No. 6, December 1972, pp. 474-480

"High-Gain 15-W Monolithic Power Amplifier With Internal Fault Protection" by Ernest L. Long and Thomas M. Frederiksen, IEEE Journal of Solid-State Circuits, Vol. SC-6, No. 1, February 1971, pp. 35-44

"Power Integrated Circuits: Problems, Tradeoffs, and Solutions" by Bruno Murari, IEEE Journal of Solid-State Circuits, Vol. SC-13, No. 3, June 1978, pp. 307-319

Examiner



Date Considered

6/17/03